# Card Shuffling Program for KIM - 1

Your 6502 might play poker like Amarillo KIM, but does it always have to pass the deal? Not if you teach it to shuffle cards!

Hark Chan P.O. Box 714 Cambridge, MA 02139

Entertaining friends with computer games certainly makes all the effort of assembling a personal computer worth-while. However, if you happen to have a small microcomputer with limited memory and very few software tools, there are not many games available. As an example, most card games need a random number generator to shuffle cards.

The standard method to generate random numbers (as used in most BASIC interpreters) is not suitable for this purpose. Since some of the bare-bone computers do not even have the software to perform multiplication, it is asking too much for them to generate floatingpoint random numbers. To make these small computers more entertaining, a simple method to shuffle cards is described here. This method is implemented in a KIM. The machine instructions use about 80 bytes. There is lots of memory left for playing card games. The only drawback is that it requires the operator to press the interrupt key in order to stop the program.

The card shuffling program consists of two portions. The second portion is the main program that shuffles cards. It just keeps on shuffling until the interrupt key is pressed. The first portion is an interrupt service routine used to ensure an orderly ending of the program. The program is relocatable, and the two portions can be in separate locations.

This feature makes it easy to incorporate the shuffling program into a complete card-playing program. However, it is important that the user initialize the interrupt vectors to jump to the interrupt service routine.

To keep the computer code relocatable, the initialization of the 2 byte address is left to the user. The storage area for the cards, together with 4 bytes of working space, are in page 0. In this program, the storage area starts at address 0001. However, the program can be changed easily to move the storage area to other locations in page 0.

The deck of cards is stored in an array at locations (hex) 0001 to 0034. The value of

0120: 0130: 0140:	0200 0200 0202		36	L1	ORG LDXIM TXA	\$0200 \$36
0150: 0160:	0203 0205	95	00		STAZX DEX	
	0206				BNE	L1
	0208				STXZ	\$38
0190:			35	LOOP	LDAZ	\$35
0200: 0210:	020C 020D		211	L2	SEC SBCIM	\$34
0210:					BCS	Ψ3Ψ L2
	0211				CLC	J_
	0212		35		ADCIM	\$35
0250:	0214				TAX	
0260:					STAZ	\$35
0270:	0217				LDAZX	•
0280:					STAZ	\$37
0290:	021B		36		LDAZ	<b>\$</b> 36
0300: 0310:					ASLA ASLA	
0310:	021E				CLC	
	0220		36		ADCZ	<b>\$</b> 36
0340:	0222		<b>J</b> 0		CLC	43.
0350:	0223		01		ADCIM	<b>\$</b> 01
0360:	0225		36		STAZ	<b>\$</b> 36
0370:	0227				CLC	
0380:	0228		35		ADCZ	<b>\$</b> 35
0390:	022A		22	L3	SEC	<b>A</b> 22
0400: 0410:	022B				SBCIM BCS	\$33 L3
0410:	022F		rb		CLC	rD
0430:	0230		34		ADCIM	\$34
0440:	0232		<b>J</b> .		TAX	73
0450:	0233		00		LDYZX	\$00
0460:	0235				LDAZ	<b>\$</b> 37
0470:	0237				STAZX	
0480:	0239				LDXZ	\$35 \$00
0490:	023B				STYZX	\$00 \$28
0500: 0510:	023D 023F				LDAZ CMPIM	\$38 \$00
0510:	0231				BEQ	LOOP
0 220 .	0E71	. 0	<b>J</b> 1		254	2001

0010:							
0020:				*	INTERRUPT	SERVICE	ROUTINE
0030:							
0040:	0243	A5	F3		LDAZ	<b>\$F</b> 3	
0050:	0245	A 4	F4		LDYZ	\$F4	
0060:	0247	A6	F5		LDXZ	<b>\$</b> F5	
0070:	0249	E6	38		INCZ	<b>\$</b> 38	
:0800	024B	40			RTI		

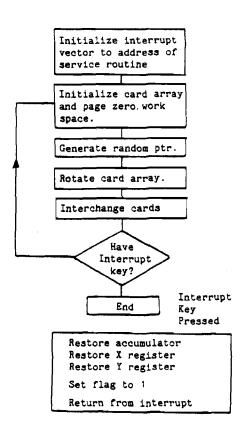
each address is distinct and is between hex 1 to 34 (decimal 1 to 52). After the interrupt key is pressed, the content of these addresses represents a deck of random cards.

The program uses a simple random number generator to generate random pointers with values between 1 and 52. The first card in the deck is interchanged with the card selected by the random pointer. The position of all the cards is next shifted one place so that the last card becomes the first, the first card becomes the second, and so on. This is to make sure that the first card is always changing, and a different card is interchanged with each randomly selected card. A random pointer is again generated and the whole operation is repeated.

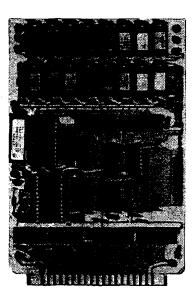
After a sufficient number of operations, the deck is suitable for card games. One or two hundred shufflings are sufficient.

When the interrupt key is pressed, the interrupt service routine sets a memory location, hex 0038, that serves as a flag to signal the end of the shufflirg. This routine also restores the accumulator and the X and Y registers. It is important that the user initialize the interrupt vector to address the service routine instead of the operating system.

The sequence of cards being shuffled is actually predetermined because it is calculated from a prescribed series of operations. However, if the stop command is activated by a human operator the cards can be very random. I takes about 10-4 second to do one shuffle. The time to activate the stop command can easily vary by more than 0.1 second. Thus, the number of shufflings can be uncertain by about 1000, which s sufficient to generate a deck of random cards.



## KIM/SYM/AIM-65-32K EXPANDABLE RAM DYNAMIC RAM WITH ON BOARD TRANSPARANT REFRESH THAT IS COMPATIBLE WITH KIM/SYM/AIN-65 OTHER 6502 BASED MICROCOMPUTERS. AND



- \*\* PLUG COMPATIBLE WITH KIM/SYM/AIA-86.
  MAY BE CONNECTED TO PET USING ADAF FOR
  CABLE SS44-E BUS EDGE CONNECTOR
  \*\* USES -SV ONLY (SUPPLIED FROM HOST
  COMPUTER BUS), A WATTS MAXIMUM.
  \*\* BOARD ADDRESSABLE IN 4K BYTE BLO:KS
  WHICH CAN BE INDEPENDENTLY PLACE! IOM
  4K BYTE BOUNDARIES ANYWHERE IN A 34K
  BYTE ADDRESS SPACE.
  \*\* BUS BUFFERED WITH 1 LS TIL LOAO.
  \*\* 200NSEC 4116 RAMS.
  \*\* FULL DOCUMENTATION
  \*\* ASSEMBLED AND TESTED BOARDS -RE
  GUARANTEED FOR ONE YEAR, AND
  PURCHASE PRICE IS FULLY REFUNDABLE! IF
  BOARD IS RETURNED UNDAMAGED WITHIN
  14 DAYS.

ASSEMBLED/ KIT TES FED \$459.00 \$389.00 WITH 32K RAM ..... 2465.00 ## 3.00
WITHOUT RAM CHIPS .... \$319.00 HARD TO GET PARTS ONLY (NO RAM CHIPS) .... \$180,00 BARE BOARD AND MANUAL . \$85.00

#### INTERFACE

CONNECTS THE ABOVE 32K EXPANDABLE RAM THE A 4K OR SK PET. EXPANSION INTERFACE CARLE BINARD STANDOFFS. POWER SUPPLY MODIFICATION KIT AND COMPLITE INSTRUCTIONS.

PO BCk 3195 ORANGE CAL FORNIA 4.5 1714 http://line

ALL ASSEMBLED BOARDS AND MEM-ORY CHIPS CARRY A FULL ONE YEAR REPLACEMENT WAXANTY

#### 16K X 1 DYNAMIC RAM

THE MEATING AND
THE MEATING IS A 16,384 BIT HIGH SPEED
NMOS, DYAAMIC RAM, THEY ARE EQUIVALENT
TO THE MOSTER, TEXAS INSTRUMENTS, OR
MOTOROLA 4116-3.
200 NSEC ACCESS TIME, 375 NSEC CYCLE
TIME

- TIME.
  16 PIN TTL COMPATIBLE.
  BURNED IN AND FULLY TESTED.
  PARTS REPLACEMENT GUARANTEED FOR

### \$9.50 EACH IN QUANTITIES OF 8

MOTOROLA MEMORY ABBRESS MULTIPLEXER— MC 3242A THE MC 3242A IS AN ADDRESS MULTIPLEXER AND REFRESH COUNTER FOR 16 PIN, 16K DYNAMIC RAMS THAT REQUIRE A 128 CYCLE

- CONTAINS MEMORY REFRESH COUNTER.
- MULTIPLEXES SYSTEM 14 BIT ADDRESS TO THE 7 ADDRESS PINS OF THE RAMS. COMPATIBLE WITH 3480 MEMORY
- COMPATIBLE
  CONTROLLER.
  PART IS GUARANTEED.
  \$12.50 EACH

# MOTORQLA BYNAMIC MEMORY CONTROLLER-

- MOTHRICA BYNAMIC MEMORY CONTROLLENMC24001.
  MEMORY CONTROLLEN DESIGNED TO SIMPLIFY
  CONTROL OF 16 PIN 4K OR 16K DYNAMIC RAMS.

  \* GENERATES RAS/CAS AND REFRESH TIMING
  SIGNALS FOR 16K TO 6K BYTE MEMORIES.

  \* GENERATES MEMORY READ/WRITE TIMING.
  DIRECT INTERFACE WITH MOTOROLA OR
  INTEL 3242A ADDRESS MUX AND REFRESH
  COUNTER.

  \* PART GUARANTEED.

  \$13.95 EACH

6502, 54K BYTE BAM AND CONTROLLER SET

- GGUZ, DAK BYTE RAM ANG CONTROLLER SET MAKE GAK SYTE MEMORY FOR YOUR 6800 OR 6502. THIS CHIP SET INCLUDES:

  3 24 MSK 4116-3 1610C1, 200 NISEC RAMS.

  1 MC3400 MEMORY CONTROLLER.

  1 MC3242A MEMORY ADDRESS MULTIPLEXER AND COUNTER.

  DATA AND APPLICATION SHEETS. PARTS TESTED AND GUARANTSED. \$325.00 PER SET

2716-450NSEC . . . .